DHIRAJLAL GANDHI COLLEGE OF TECHNOLOGY
DEPARTMENT OF ECE
VLSI LABORATORY

DESCRIPTION OF THE LAB

The students learn to design digital circuits using the various hardware description languages and also their implementation on FPGAs.The students are able to design, simulate, extract the layouts of analog IC blocks using EDA tool.

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MAJOR EQUIPMENTS
Hardware

- SPARTAN 3E FPGA kits

Software

- Tanner (v16.1)
- XILINX ISE design (v14.5)